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Design, Implementation and Comparison of Mix **Column Architecture using Different Techniques**

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Abstract: Cryptography is a technique of securing the data from hackers. The most popular method employed for cryptography is Encryption. Many researchers around the world have contributed towards improvising encryption methods. This paper explains the two architectures with which the data can be encrypted.

Keywords: AES, VLSI, Cryptography, LUT, Splitting

I. INTRODUCTION

Technology has changed our lives in many aspects - from AES algorithm is an iterative method to compute the the advent of natural user interfaces [1], [2], safeguarding cipher text of the message, which is always fixed to be a our lives [3], and in helping those who are physically 16 byte (128bits) input. The number of iterations usually weak/challenged [4], [5], [6]. It has also changed the mode depends on the length of the key used which can be of of communication from hand-written letters to wired 128,192,256 bits and corresponding rounds to be 9, 11 and communication to wireless [7], [8], [9]. Now-a-days, most 13 respectively. Fig 1 shows the steps performed in each of the communication modes are dependent on Internet. Even the secret messages are being stored and sent through servers.

It is noticeable that in recent past, the rate of Internet hacking has increased. Thus, securing data using cryptographic techniques is gaining more importance nowa-days. Cryptography involves converting a message text into an unreadable cipher, using hidden writing maintaining security and privacy [10]. A large number of and Decryption algorithms Encryption for the cryptography are available. In order to keep pace with maturity of the security technology such as the hackers, the electronic eavesdroppers, electronic frauds and the virus have been coming into the field with new improved techniques for to attack the security mechanism [11].

Advanced Encryption Standard (AES) was adopted as a standard by the US government, a symmetric-key encryption method (Encryption and Decryption using same key) succeeding the DES (Data Encryption Standard) and 3DES (3-times DES). AES has 10 rounds of complex algebraic and matrix operation which involve high processing power and introduce delay in encryption and decryption process. The sub module Mix Column operation in AES provides the maximum confusion with the data involving Galois field Multiplication. Galois field multiplication is one of the complex operations involving polynomial division after the multiplication for its reduction.

The paper discusses about improvising the AES module. The following are the sections involved in the paper: Section II describes the brief AES Algorithm, Section III describes Mix Column Transformation, Section IV showcases Conventional approach; Section V portrays Look up Table approach. Section VI summarizes on the Results and Section VII concludes the paper.

II. AES ALGORITHM

of the iteration.



Fig 1. AES Algorithm

The following are the steps involved in AES:

• Add Round Key: The input data is XORed with the key. • Byte Substitution: Using S-box table, each byte is replaced with the corresponding one in the table.

• Shift Row: Each row of the array is rotated by a certain number of byte positions. No shift in first row, one left shift of bytes in the second row, two left shifts of bytes in the third row, and three left shift operation in the last row.

• Mix Column Transformation: Discussed in next section.

III.MIX COLUMN TRANSFORMATION

In AES, Mix Column operation is implemented with the help of the diffusion operation which shuffles the data based on mathematical model on Galois Field [12]. This is one of the most power hungry modules which is operated as nonlinear process that makes use of arithmetic over Galois Field (28) to provide the maximum confusion. In





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this operation, the input data is represented by an individual 4x1 matrix, which is multiplied (GF) by a constant 4x4 matrix shown Fig 2. Thus, the coefficients of each term of the polynomial can take the value 0 or 1. Here, we have considered GF (28), which is modulo 8. Any hexadecimal value can be represented by bit string of length 8, each bit corresponding to a binary weighted bit value. The LSB of the coefficient represents the constant of the polynomial, and going from right to left, the coefficient of x_i is represented by the bit b_i where b_i is i bits to the left of the least significant bit.



Fig 2. Mixcolumn transformation during encryption

As an example, the bit string 10101011 represents $x^7+x^5+x^3+x^{1}+1$. In the polynomial representation, multiplication in GF (2⁸) corresponds with the multiplication of polynomials modulo an irreducible polynomial of degree 8. A polynomial is irreducible if its only divisors are one and itself. For the AES algorithm, the irreducible polynomial is given by the equation $m(x) = x^8 + x^4 + x^3 + x^1 + 1$.

Example: Multiplication of two polynomials, in this example leads to result of a new polynomial which has degree greater than 7 that is 10.

$$\begin{array}{r} x^{4} + x + 1 \\ \times & x^{6} + 1 \\ \hline & \\ & \\ & \\ & \\ \oplus & \\ x^{10} + x^{7} + x^{6} \\ \hline & \\ & \\ x^{10} + x^{7} + x^{6} + x^{4} + x + 1 \end{array}$$

Fig 3: Multiplication of two polynomials

In order to reduce it to a polynomial less than 8, we perform polynomial division by an irreducible constant $x^8 + x^4 + x^3 + x^1 + 1$, followed by XOR operation between the dividend and the product of divisor and quotient. The remainder is checked again for having degree greater than 7, if so then it is again divided by the irreducible constant. This process is continued till the remainder is less than of degree 8.

$$\frac{x^{8} + x^{4} + x^{3} + x + 1)x^{10} + x^{7} + x^{6} + x^{4} + x + 1}{\frac{x^{10} + x^{6} + x^{5} + x^{3} + x^{2}}{x^{7} + x^{5} + x^{4} + x^{3} + x^{2} + x + 1}}$$

Fig 4. Division of two polynomials

IV.INVERSE MIX COLUMN OPERATION

The reverse process of the Mix Column transformation is the Inverse Mix Column. Inverse Mix Column operates on the State column by column, treating each column as a four term polynomial. Here the constants in polynomial can be created similarly as in mix column operation. The multiplying constant matrix varies in this inverse operation and is as shown in Fig 5.

$\int 0E$	0 <i>B</i>	0D	09	$\begin{bmatrix} w'_3 \end{bmatrix}$		$\begin{bmatrix} w_3 \end{bmatrix}$
09	0 <i>E</i>	0 <i>B</i>	0D	w2'	E.	w_2
0D	09	0 <i>E</i>	0 <i>B</i>	w'1		w
0B	0D	09	0E	wo		wo

Fig 5. Mixcolumn transformation during decryption

V. CONVENTIONAL APPROACH

In this approach, we use normal 8 bit multiplier to obtain the product, before polynomial division. It is found that Baugh Wooley multiplier is more efficient compared to other multipliers. Fig 6 shows the block diagram of the Baugh Wooley Multiplier for 5 bit, the same can be extended for 8 bit.



Fig 6. 5X5 Bit Baugh Wooley multiplier

VI.LOOK UP TABLE APPROACH

In this approach, two look up tables are defined with 256 values each. Each entry is an 8bit value. The multiplication of Galois Field is achieved by addition of the value obtained from the look up table. If the obtained result is greater than GF (2^8) , then 256 is subtracted from it. The result value is retrieved back in another look up table, to obtain the final result.

Method: If the two Hexadecimal values being multiplied are CF &18, using LUT A1 (CF) index which returns D1, and then that of second value A1 (18) which returns 4C. Now add the values, D1+4C = 11D. Since 11D > FF, we perform: 11D-FF which gives us 1E. Using LUT A2 (1E) = 66. Thus, the end result of multiplying CF &18 over a Galois Field is 66.



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VII. RESULTS

Table 1 shows the comparison between the mix column architecture using two different approaches.

TABLE I COMPARISON OF DIFFERENT APPROACHES

Approach	No of LUT's	Path Delay
Baugh Wooley	112	7.4775ns
Look Up Table	87	19.059ns

VIII. CONCLUSION

The table clearly indicates that the area consumed by the LUT approach, as well as speed of operation is comparatively more than Baugh Wooley approach.

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REFERENCES

- P. C. Ravoor, B. S. Ranjani and S. Rao Rupanagudi, "Optimized fingertip blob recognition for image processing based touchscreens," Recent Advances in Computing and Software Systems (RACSS), 2012 International Conference on, Chennai, 2012, pp. 104-108.
- [2] P. C. Ravoor, S. R. Rupanagudi and B. S. Ranjani, "Detection of multiple points of contact on an imaging touch-

screen," Communication, Information & Computing Technology (ICCICT), 2012 International Conference on, Mumbai, 2012, pp. 1-6.

- [3] S. R. Rupanagudi et al., "A novel video processing based smart helmet for rear vehicle intimation & collision avoidance," 2015 International Conference on Computing and Network Communications (CoCoNet), Trivandrum, 2015, pp. 799-805.
- [4] Rupanagudi SR, Bhat VG et al (2015) Design and Implementation of a Novel Eye Gaze Recognition System Based on Scleral Area for MND Patients Using Video Processing. Advances in Intelligent Informatics. doi:10.1007/978-3-319-11218-3_51
- [5] Rupanagudi SR, Bhat VG et al (2015) Design and Implementation of a Novel Eye Gaze Recognition System Based on Scleral Area for MND Patients Using Video Processing. Advances in Intelligent Informatics. doi:10.1007/978-3-319-11218-3_51
- [6] S. R. Rupanagudi et al., "A novel video processing based cost effective smart trolley system for supermarkets using FPGA," Communication, Information & Computing Technology (ICCICT), 2015 International Conference on, Mumbai, 2015, pp. 1-6.
- [7] S. R. Rupanagudi et al., "Design of a low power Digital Down Converter for 802.16m - 4G WiMAX on FPGA," Advances in Computing, Communications and Informatics (ICACCI, 2014 International Conference on, New Delhi, 2014, pp. 2303-2308.
- [8] S. R. Rupanagudi et al., "A low area & low power SOC design for the baseband demodulator of an indoor local positioning system," 2015 International Conference on Computing and Network Communications (CoCoNet), Trivandrum, 2015, pp. 689-695.
- [9] S. R. Rupanagudi, Ranjani B. S., P. Nagaraj, V. G. Bhat and Thippeswamy G, "A novel cloud computing based smart farming system for early detection of borer insects in tomatoes," Communication, Information & Computing Technology (ICCICT), 2015 International Conference on, Mumbai, 2015, pp. 1-6.
- [10] K.J. Jegadish Kumar, R. Balasubramanian, "Lightweight Mixcolumn Architecture for Advanced Encryption Standard", International Journal of Computer Applications (0975 – 8887) Volume 136 – No.11, February 2016
- [11] Sliman Arrag, Abdellatif Hamdoun, Abderrahim Tragha, Salah eddine Khamlich, "Design and Implementation A different Architectures of mixcolumn in FPGA"
- [12] Rajasekar P, Dr. H. Mangalam, "Design of Low Power Optimized MixColumn/Inverse MixColumn Architecture for AES", International Journal of Applied Engineering Research ISSN 0973-4562 Volume 11, Number 2 (2016) pp 922-926